Degree Program in Electrical Engineering Department, Chang Gung University (For 2024 Calendar Year Admission) Freshman Sophomor Junior Senio Course Title Course Title Course Title Course Title 1st 2nd 3rd 1st 2nd 3rd 1st College Elective glish Speaking and Presentation 2 Engineering Mathemati Differential Equations) 3 3 3 alculus(I)(I) ectronics Laboratory (🗓) ring Mathematics (Linear 3 3 3 eneral Physics(I)(II) Electromagnetics(I) 1 eneral Physics Laboratory(I)(I Flectric Circuits (TI) enior Design Project(I)(II) Electronics(I)(II) General Logic Design 3 3 ectronics(III) 3 Requirement Computer Programming 1 1 troduction to Electrical 2 Ingineering ectric Circuits(I) 3 ntroduction to Engineerin 1 Probability & Statistics Numerical Method 1 4 Electronics Circuits Design* Hardware Description Language 3 Introduction to Deep Learning 3 Ohiect-Oriented Programming oftware-Hardware Co-design 3 Design and Implementation of Proc 3 3 College Electiv yout of Printed Cricuit Boar ctor Analysis and Complex 3 Course 3 Computer Organization Hand-on for Deep Learning-3 3 3 Mobile Device Programming 3 1 3 ensing Circuit Systems* W/SW AI IC Co-Design and E Next Generation Intelligent Communication Technology and IOT 2 2 APP Programming estem Experiment esign and Implementation of Embedo o Intellectual Intelligent Sensing and Recognit Smart Sensor Network 4 n micatio 2 and IoT Systen Thing practice

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Applications of Information and Application Technology 2 Course Digital Communication Computer Networks Optical Fiber Communications* 3 troduction to Internet Sec Vireless Network® ntroduction to Digital Com error-Control Coding® 1 Introduction to Digital Signal Proce Intelligent IoT Sensing and its 3 Digital Signal Processing® 3 Digital Signal Processing Lab+ Random Processes 0 E S 3 S Ι Introduction to Next Gen Mobile Communications 0 N applications* 3 Electric Machiner Linear System Theory utomatic Control 3 Power Electronics Labora 1 Electrical Power Systems 3 Advanced Power Electronics 3 Power and 3 Е C 3 3 Digital Control* 3 Т 3 FPGA System Design Lab-VLSI System Design⁴ Е Engineering 3 3 Digital Integrated Circuit Design С 3 3 Integrated 0 ntroduction to Analog Design 3 AI Chip Design* 3 circuit n Memory Computing IC Design Ū Systems emory IC Design and Е Introduction to Medical Electronic 3 Electrical Instrument and Lab+ S 3 Digital Signal Processing Lab 1 Microprocessor Application and Lab.+ Intelligent IoT Sensing and its Medical Information System* 3 Digital Signal Processing® 3 Biomedical Digital Image Processing* ntroduction to Optoelectron 1 3 Bio-photonics Techniqu Embedded System and . Graduation Credits: Minimum of 133 credits (1) Required Courses: 60 credits. • (2) Elective Courses: 44 credits: A. Department Electives: Minimum of 38 credits. B. Non-departmental Electives: Maximum of 6 credits recognized as graduation credits (General Education courses, Physical Education, National Defense Education, Military Training elective courses, retake courses, and courses for transfer (departments) or makeup courses are not included). C. Undergraduate students may take "Embedded System Design and Implementation" and use it to fulfill the elective experimental course requirements o for Electrical Engineering students' graduation. (3) General Education Credits: Please refer to the regulations of the General Education Center. English field, core, and diverse courses: 29 credits $\,^\circ$ M M E . Physical Education: Mandatory 0 credits for freshman and sophomore years. . [Focused Campus] Required 0 credits. Please refer to the Office of Academic Affairs Focused Campus section for details. 4. The university has set an English graduation threshold. Graduation is contingent upon meeting the set standards. Please refer to the Language Center T regulations for details Others:: $A.\ Apart\ from\ mandatory\ lab\ courses,\ students\ must\ take\ at\ least\ 4\ lab\ courses\ marked\ with\ a\ "+".\ Independent\ Study\ (1)\ and\ Independent\ Study\ (2)$ can count as 1 lab course. (Courses recognized as lab courses are marked with "+"). B. Restrictions on prerequisite and follow-up courses are as follows: Hardware Description Language-FPGA System Design Lab, Electrical Machinery- $Electrical\ Machinery\ Lab,\ Control\ Engineering\ - Control\ Engineering\ Lab,\ Fiber\ Optic\ Communication\ - Fiber\ Opt$ Senior Design Project (1)- Senior Design Project (2), Introduction to Optoelectronic Engineering-Optoelectronic Lab. C. Courses marked with "*" are subjects jointly offered for seniors and master's students.